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8 string instructions of 80865_Arithmetic Instructions of 8086 [AAA, AAS, AAM, AAD, DAA, DAS] 8086 | Block Transfer Program | String Instructions | Bharat Acharya Education *8086 Rotate, Shift and Processor Control Instructions | Bharat Acharya Education* **8086 | Programming Part 4 | Bharat Acharya Education**
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Complete 8086 Instruction Set Chettinadtech Complete 8086 instruction set by Alexander Popov. Quick reference: AAA AAD AAM AAS ADC ADD AND CALL CBW CLC CLD CLI CMC CMP: CMPSB CMPSW CWD DAA DAS DEC DIV IDIV IMUL IN INC INT INTO IRET JA: JAE JB JBE JC ... assembler generates several RCL xx, 1 instructions because 8086 has machine code

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Most processors have a "compare" instruction. look at the instruction reference for whatever processor you're trying to program, and use the instruction you find. compare two operands (cmp) cmp{bwl} reg[8]16[32], r/m[8]16 cmp is typically executed in conjunction with conditional jumps and the setcc instruction. example. Complete 8086 instruction set example: mov al, 5 mov bl, 5 cmp al, bl ; 1 instructions because 8086 has machine code only for this instruction lea instruction in 8086 ...

8086 cmp example in instruction - johannafaith.com

Microprocessor - 8086 Instruction Sets - The 8086 microprocessor supports 8 types of instructions ? ... STI ? Used to set the interrupt enable flag to 1, i.e., enable INTR input. CLI ? Used to clear the interrupt enable flag to 0, i.e., disable INTR input. Iteration Control Instructions.

Microprocessor - 8086 Instruction Sets - Tutorialspoint

complete 8086 instruction set. complete 8086 instruction set. quick reference: aaa aad aam aas adc add and call cbw clc cld cli cmc cmp: cmpsb cmpsw cwd daa das dec div hlt idiv imul in inc int into iret ja: jae jb jbe jc jcxz je jg jge jl jle jmp jna jnae jnb: jnbe jnc jne jng jnge jnl jnle jno jnp jns jnz jo jp jpe: jpo js jz lahf lds lea les ...

Complete 8086 instruction set - KFUPM

Original 8086/8088 instruction set Instruction Meaning Notes Opcode AAA: ASCII adjust AL after addition: used with unpacked binary coded decimal: 0x37 AAD: ASCII adjust AX before division: 8086/8088 datasheet documents only base 10 version of the AAD instruction (opcode 0xD5 0x0A), but any other base will work. Later Intel's documentation has the generic form too.