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Design, by Brown & Vranesic, 2nd ed (2012),

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Fundamentals of digital logic with Verilog design / Stephen D. Brown, Zvonko G. Vranesic.—1st ed. p. cm. (McGraw-Hill Series in electrical and computer engineering)  
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Design, 3/e, Stephen Brown, Zvonko Vranesic,  
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Design (McGraw-Hill Series in Electrical and Computer Engineering) 2005. Abstract. No abstract available. Cited By. El-Bakry H Fast Karnough map for simplification of complex Boolean functions Proceedings of the 10th WSEAS international conference on Applied computer science, (478-483) Panato A, Silva S ...

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